

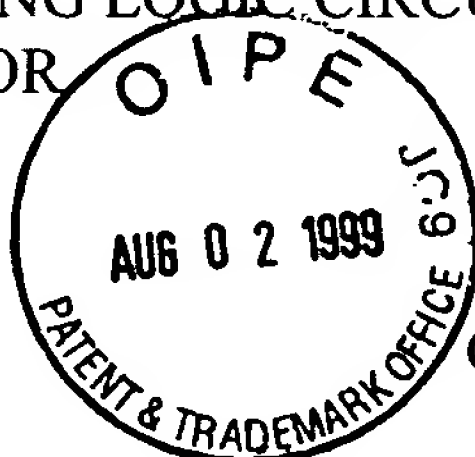
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Tremblay, Marc

Assignee: Sun Microsystems, Inc.

Title: A GROUPING LOGIC CIRCUIT IN A PIPELINED SUPERSCALAR  
PROCESSOR

Serial No.: 08/662,582



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Examiner: G. Patent

Group Art Unit: 2783

Docket No.: SP-1391 US

San Jose, California  
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Box CPA  
ASSISTANT COMMISSIONER FOR PATENTS  
Washington, D. C. 20231

**INFORMATION DISCLOSURE STATEMENT**

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, Applicant brings the documents listed in the attached PTO form 1449 to the Examiner's attention in the above-captioned application. These documents were cited in the EP Search Report (attached) of corresponding European application no. 9730383. Copies of these documents are enclosed.

Citation of the above documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described above; or

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3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

Respectfully submitted,



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